

Method for Parametrizing an Integrated Circuit and an Integrated Circuit Therefor

Abstract

A method is described for parametrizing an integrated circuit by applying a digital start command signal followed by a parametrization data signal to the supply voltage terminal and/or the output terminal of the integrated circuit. During the parametrization process, the voltage level applied to the supply voltage terminal and/or the output terminal is kept above the normal operating voltage level and detected by a detector device provided in integrated circuit. The integrated circuit includes the supply voltage terminal, a reference potential terminal, and the output terminal, as well as an internal memory which is preferably non-volatile. The adjustment specification for parametrizing the integrated circuit is stored in the memory and activated by the parametrization data signal.